

# LOAD BALANCE ALGORITHM RESEARCH ON PARALLEL PARTITIONED MULTI-BLOCK STRUCTURED OVERLAPPING GRID

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**Key words:** Computational fluid dynamics; Overlapping grid; Parallel computation; Load balance algorithm.

**Abstract:** Grid technologic acts as a crucial role in Computational fluid dynamics. As decreasing the demand of topology and the difficulty of grid generation efficiently, overlapping grid has been widely used in the past 30 years. The efficiency demand of parallel computation needs the extending of serial overlapping algorithm to parallel environment and well load balance. Based on the efficient, robust and fully automatic grid assembly approach on multi-block cell-centred structured grid for massively parallel computation, two load balance algorithms are designed to solve the imbalanced problem. A test case is applied to test those load balance algorithms. The comparison of speed-up and detail of grid transport quantity is presented and shows that the improved load balance algorithm achieves good speed-up. Meanwhile, there is still some optimization remaining to be done in the future work.

## 1 INTRODUCTION

The overlapping grid assembly method is widely investigated. Currently, there are several developed grid assembly packages such as PEGASUS5<sup>[1]</sup>, SUGGAR++/DiRTlib<sup>[2]</sup>, OVERFLOW<sup>[3]</sup>, BEGGAR<sup>[4]</sup>, FASTRAN<sup>[5]</sup>, Overture<sup>[6]</sup>, and ElsA<sup>[7]</sup>. Meanwhile, some teams devoted themselves to improve the overlapping grid assembly method and efficiency. Based on the grid size of decomposed domain, Kima<sup>[8]</sup> developed an overlapping grid assembly approach. Landmann<sup>[9]</sup> extended the Implicit Hole Cutting (IHC)<sup>[10,11]</sup> algorithm to the parallel environment. However, most of their parallel efficiency is less than desirable. Partitioning some of the partitioned grids again, Roget<sup>[12]</sup> proposed an adaptive load re-balance method on partitioned unstructured grids to achieve better load balance. However, this method is not suitable for partitioned structured grids because the disordered distribution of those grids on processors will lead to the discontinuity of grids in space. The grid re-partition algorithm by applying a volumetric division cannot deal with the discontinuous partitioned structured grids. Fig. 1 shows a typical example of partitioned grids in a parallel environment. Different colors represent different processors. It is clear that the grids of a body distributed to a specific processor do not continue generally, especially for near body grids. It is the most significant difference with the partitioned unstructured grids. After dealing with the overlapping process on parallel environment, how to achieve a high efficiency in the overlapping process is another crucial aspect. Based on our improved overlapping grid

approach, three load balance algorithms are achieved and some details of grid transportation among processes will be discussed in this paper.

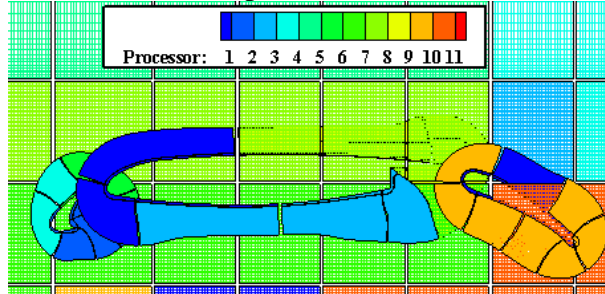


Fig. 1 A multi-block structured partitioned grids in the parallel environment

## 2 LOAD BALANCE ALGORITHMS

### 2.1 Without load balance

Donor search is a crucial step in the overlapping grid assembly method. The purpose of donor search is to determine the donor cells of query points and their interpolation weights. Through Query point identification, the target processors of all query points should be sent to for donor search are picked out. Without any load balance consideration, the query points are packed and sent to their target processors directly.

### 2.2 Initial load balance algorithm

The first step of initial load balance algorithm is to estimate the load for every processor. As mentioned before, the number of query points per processor is used as the criterion of load balance. With the help of the above query point identification method, the number of query points of every Cartesian cell can be estimated easily. Then the average load ( $La$ ) among all processors is computed as follow:

$$La = \sum_{m=1}^{nobj} \cdot \sum_{(i,j,k) \text{ on } AG_m} N_{i,j,k,m}^{query} / N \quad (1)$$

where  $nobj$  is the number of the bodies,  $N_{i,j,k,m}^{query}$  is the number of query point located in the  $(i,j,k)$  Cartesian cell of  $AG_m$  and  $N$  is the number of processors.

The second step is to redistribute load for every processor. The load on every cell of AGs is clear and every AG cell is marked as target ID where its query points should be sent for donor search orderly. The algorithm is illustrate in Fig 2.

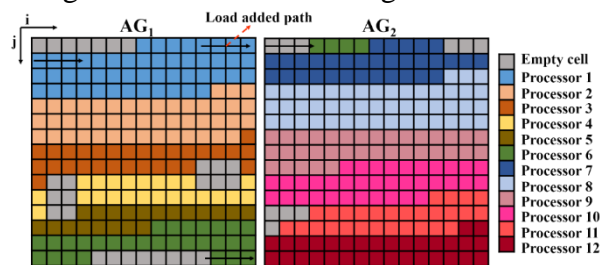


Fig. 2 Initial load balance algorithm in 2D

### 2.3 Improved load balance algorithm

For a query point, the computation cost also depends on the number of ADT nodes. The necessary cost to solve the geometric searching and intersection problems is found to be proportional to  $\log(N^*)$  where  $N^*$  is the number of ADT nodes. Then the load on the  $(i, j, k)$  Cartesian cell of  $AG_m(L_{i,j,k}^m)$  is determined as follow:

$$La = \sum_{m=1}^{nobj} \cdot \sum_{(i,j,k) \text{ on } P_n} N_{i,j,k,m}^{query} \cdot \log \sum_{(i,j,k) \text{ on } P_n} N_{i,j,k,m}^{ADT} \quad (2)$$

where  $nobj$  is the number of bodies. Different from the above algorithm which only uses the number of query points as the load, this average load ( $La$ ) cannot be estimated easily. Therefore, the distribution algorithm needs to be redesigned as follows.

As the similar distribution process, the illustration of improved load balance algorithm is shown in Fig3.

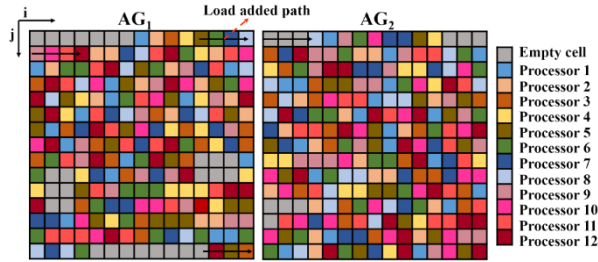
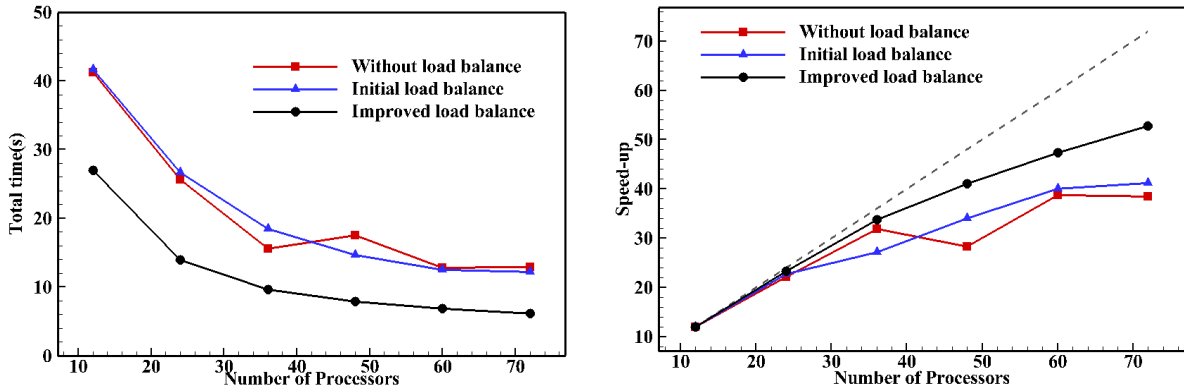


Fig.3 Improved load balance algorithm in 2D

### 3 TEST CASE AND CONCLUSIONS

The 30P30N airfoil is applied to test the efficiencies of those load balance algorithms. The total number of grids is 27 million. This case runs on 48 processors (2.6 GHz Intel Xeon E5-2650 Sandy Bridge processors). Fig 4 shows the variation of speed-up and total time required to perform the grid assembly of those three algorithms, for different numbers of processors (12, 24, 36, 48, 60 and 72). The improved load balance algorithm keeps the speed-up very well.



(a) Total time of overlapping grid assembly                      (b) Speed-up of overlapping grid assembly  
Fig.4 Time to perform of three overlapping grid assembly algorithms

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